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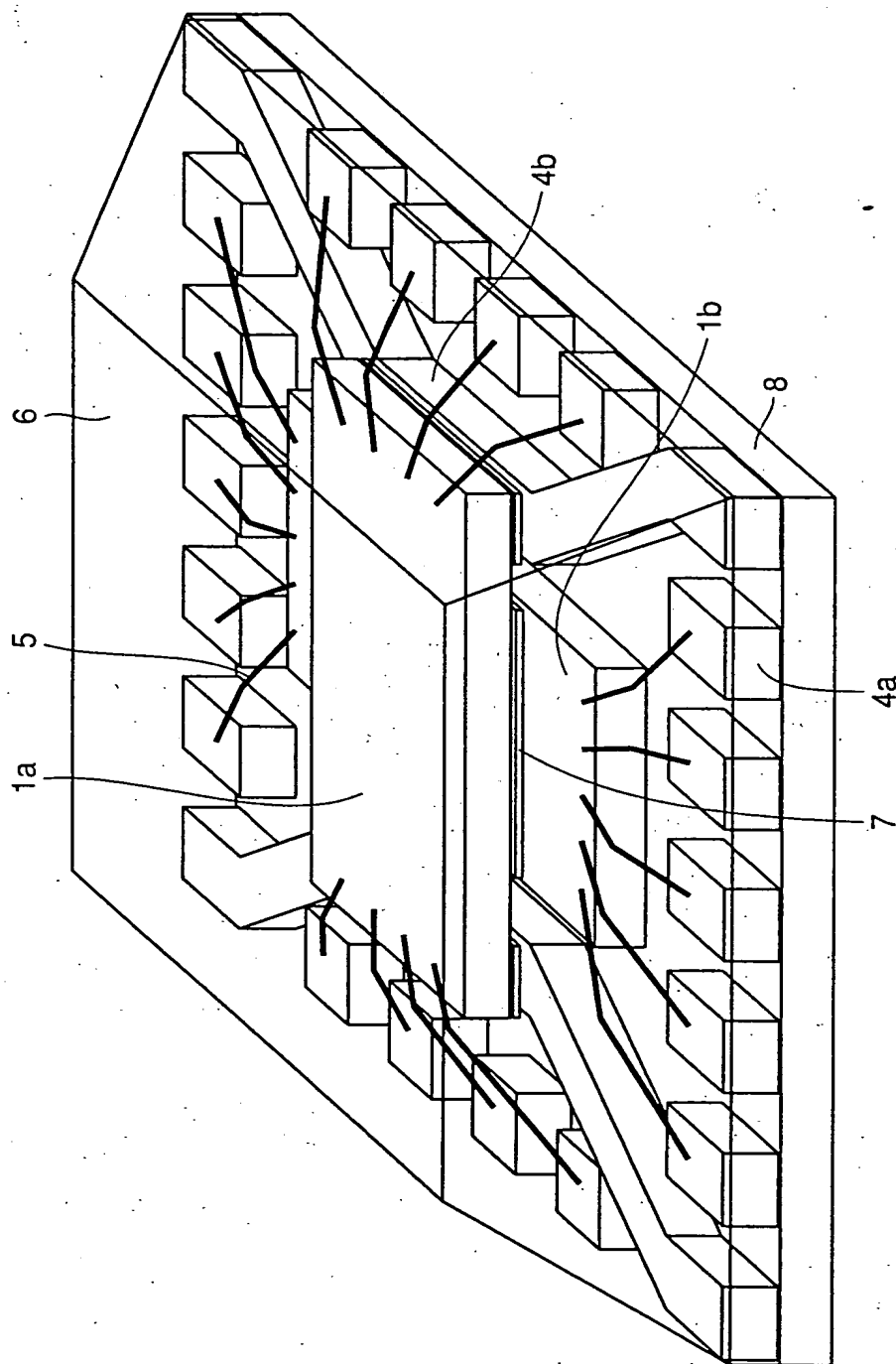


FIG. 2

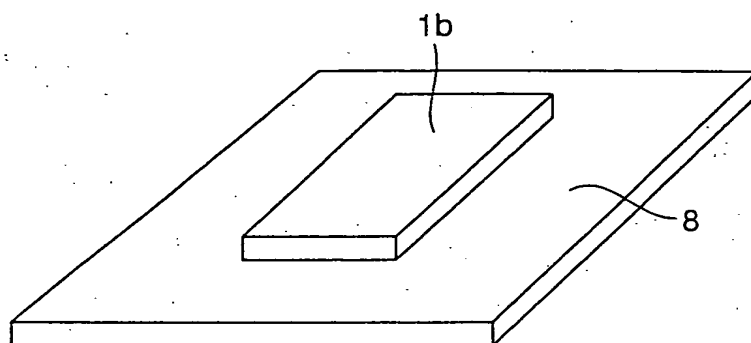


FIG. 3

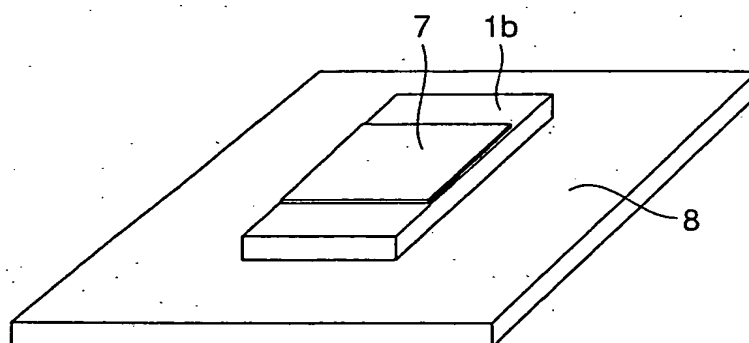


FIG. 4

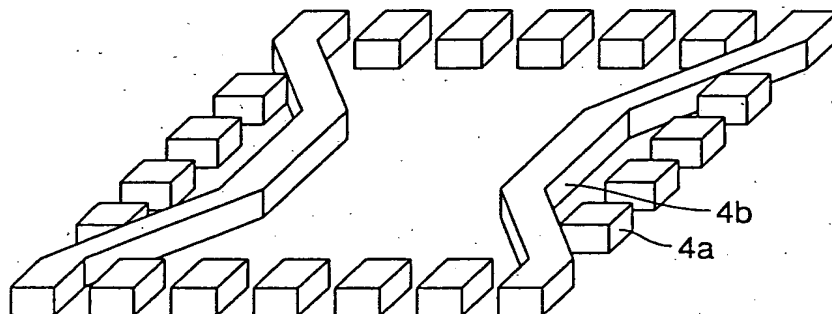


FIG. 5

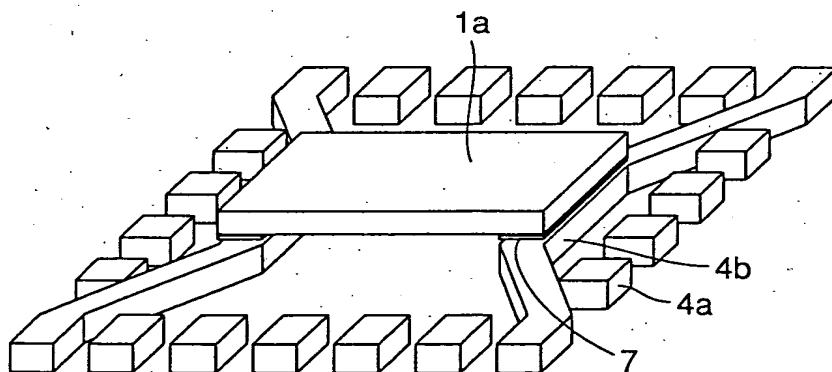


FIG. 6

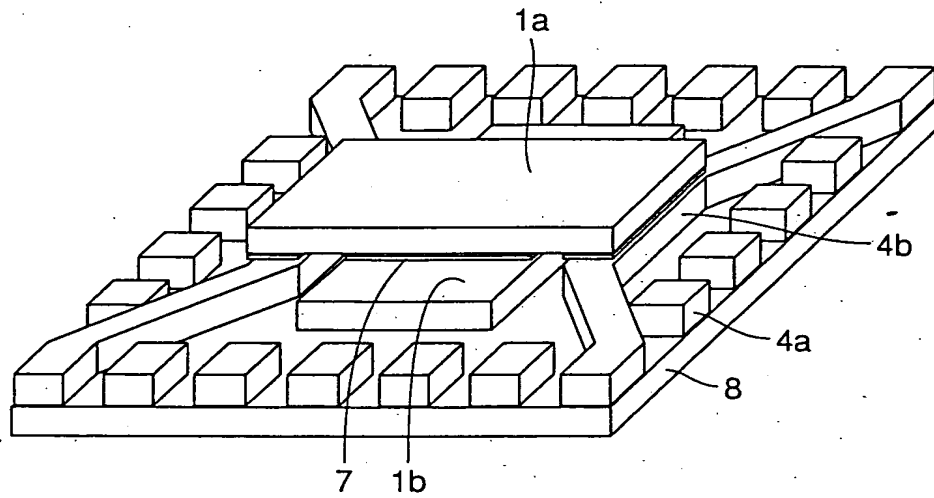


FIG. 7

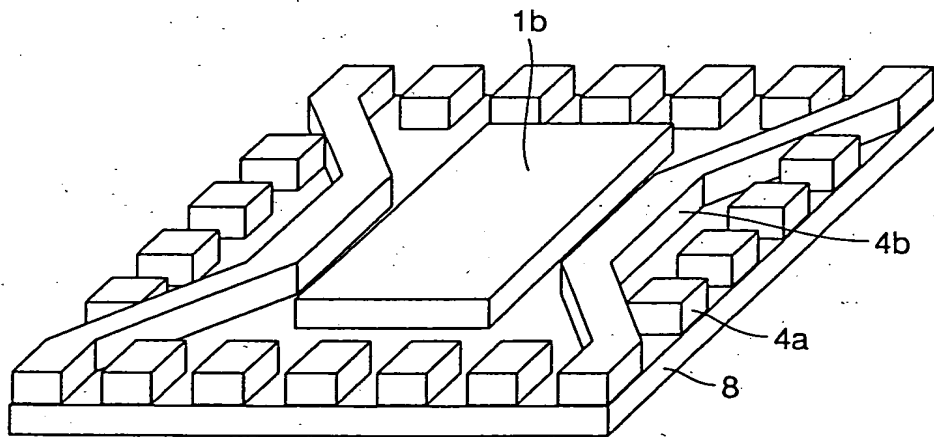
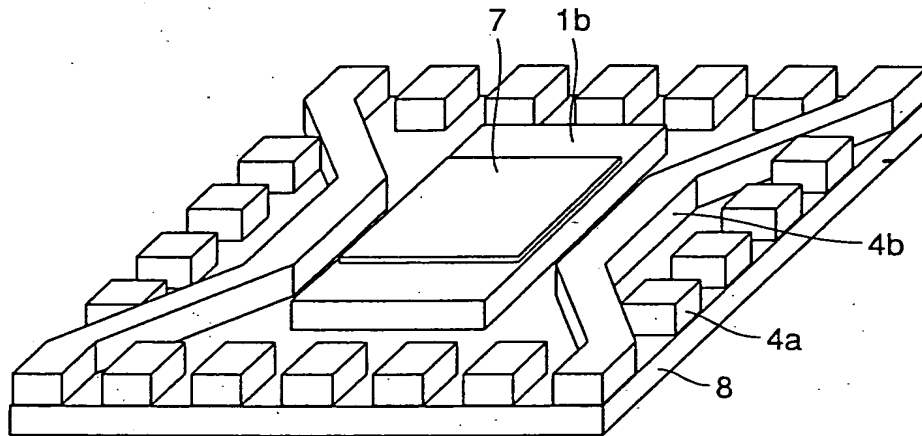


FIG. 8



Top view of a semiconductor device 100. The device features a central square region 1a. Surrounding this central region are four sets of parallel bars: 1b on the left, 2a on the top, 2b on the bottom, and 3a on the right. The device is enclosed within a frame 12. Arrows A, B, C, and D indicate directions. Labels 1a, 1b, 2a, 2b, 3a, 3b, 4a, 4b, and 12 are used to identify components.

FIG. 10

A-A'

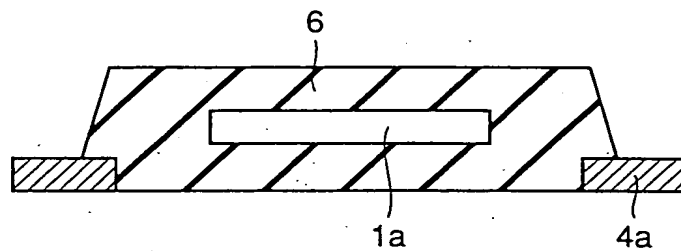


FIG. 11

B-B'

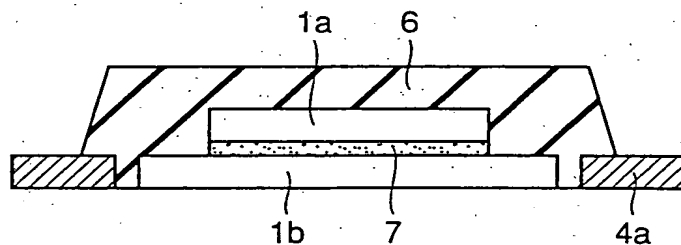


FIG. 12

C-C'

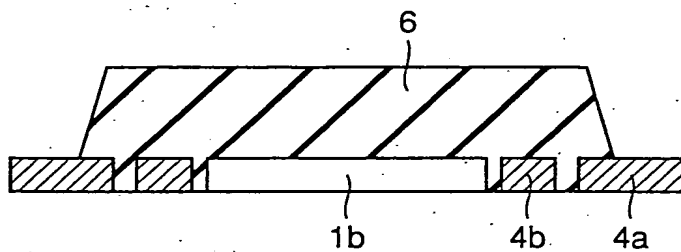


FIG. 13

D-D'

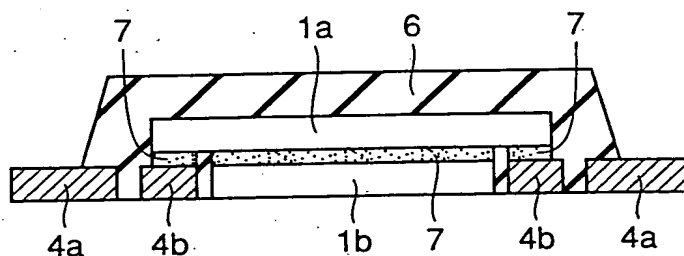


FIG. 14

A-A'

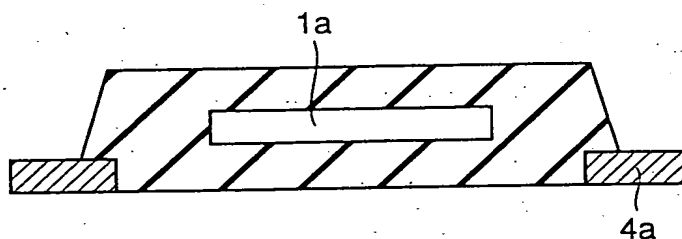


FIG. 15

B-B'

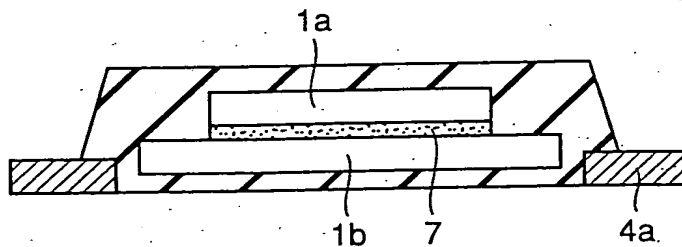


FIG. 16

C-C'

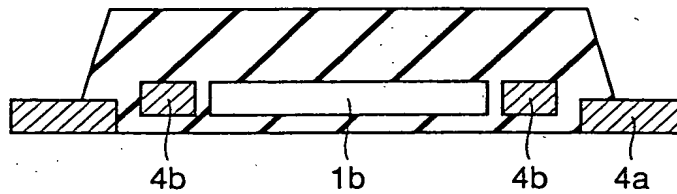


FIG. 17

D-D'

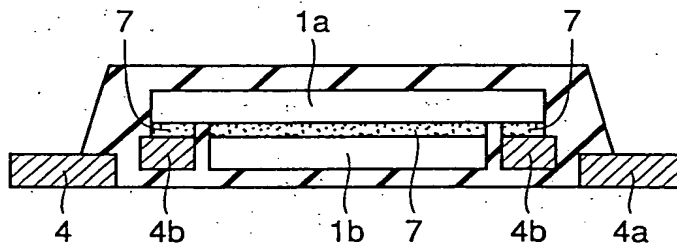


FIG. 18

A-A'

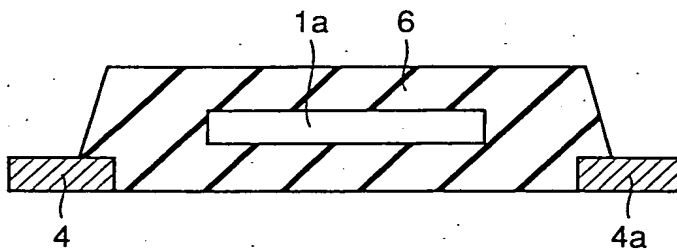


FIG. 19

B-B'

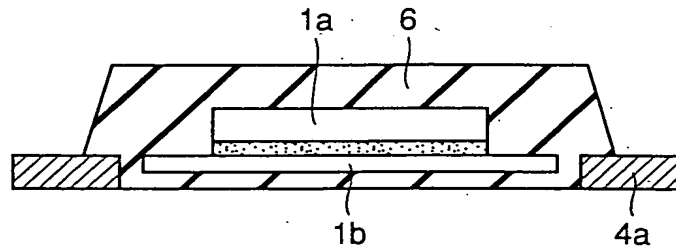


FIG. 20

C-C'

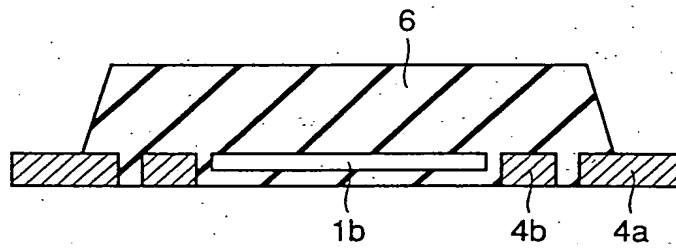
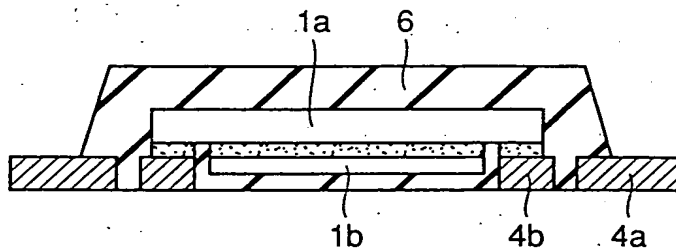


FIG. 21

D-D'



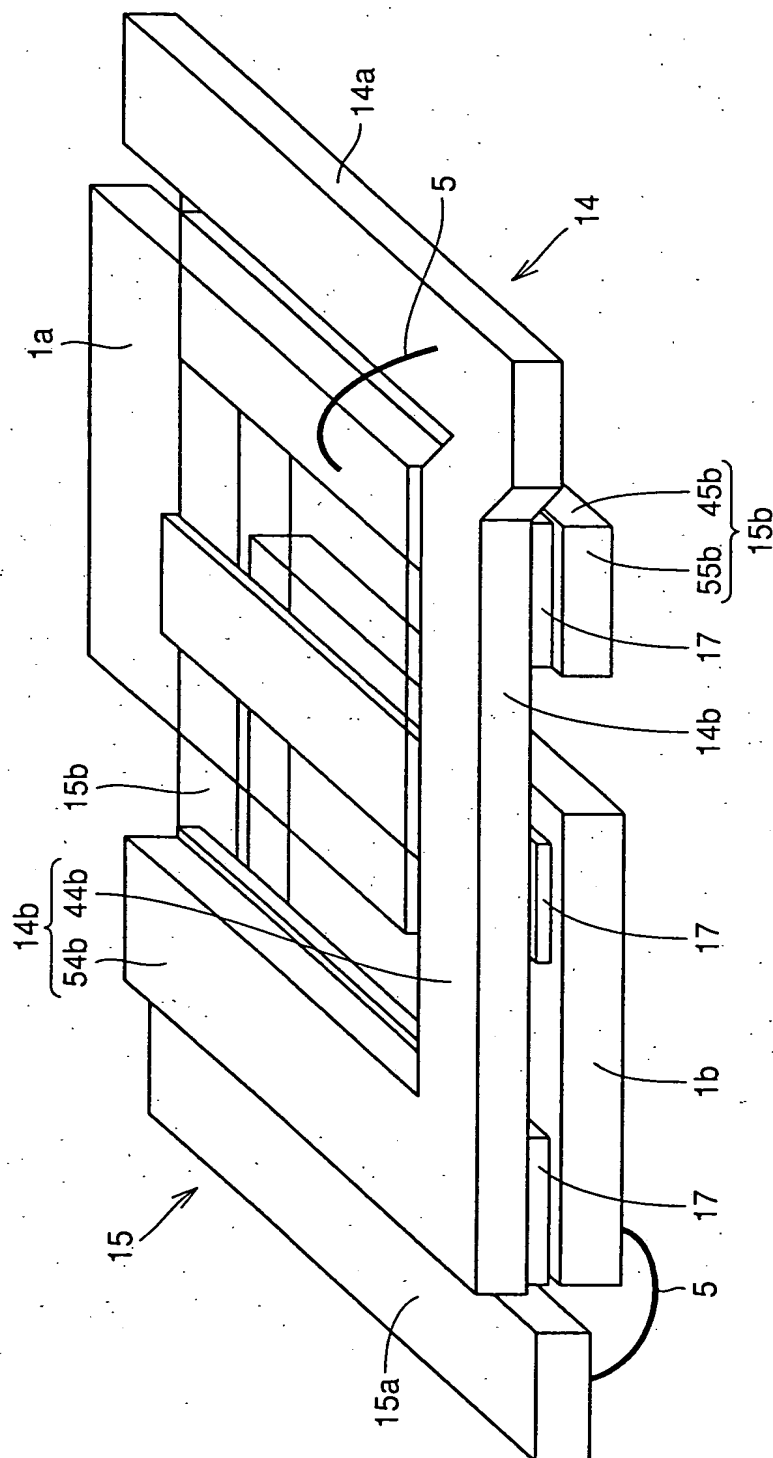


FIG. 22

FIG. 24

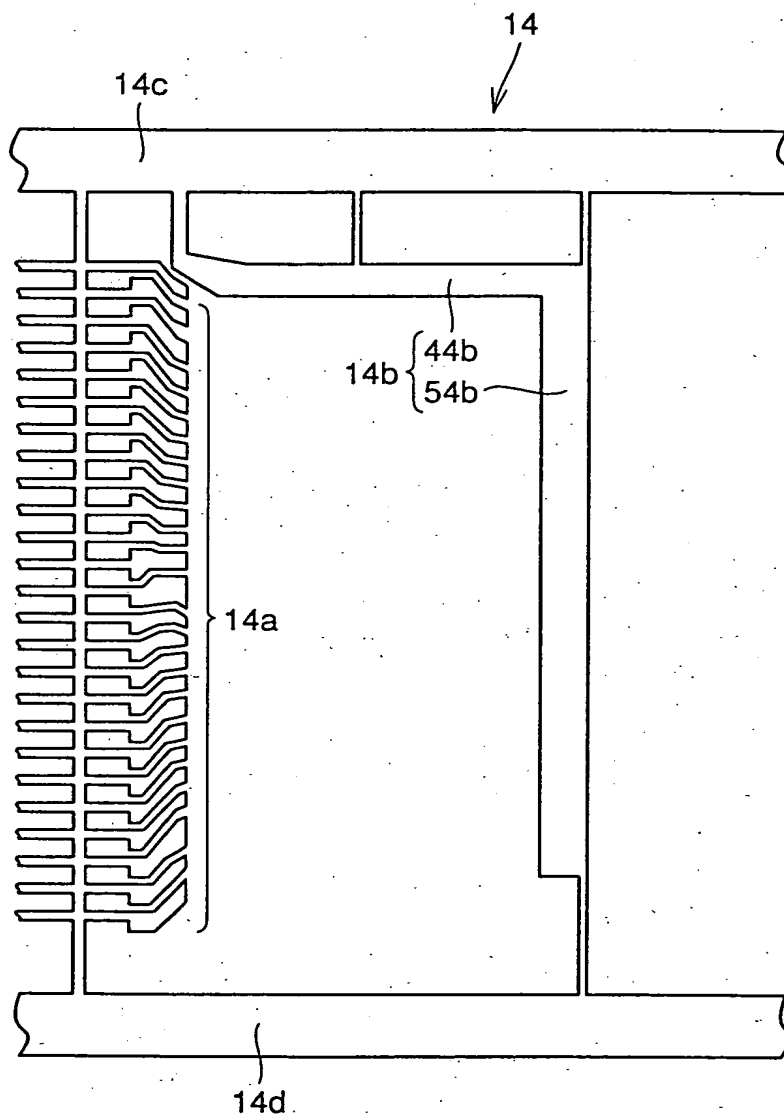


FIG. 25

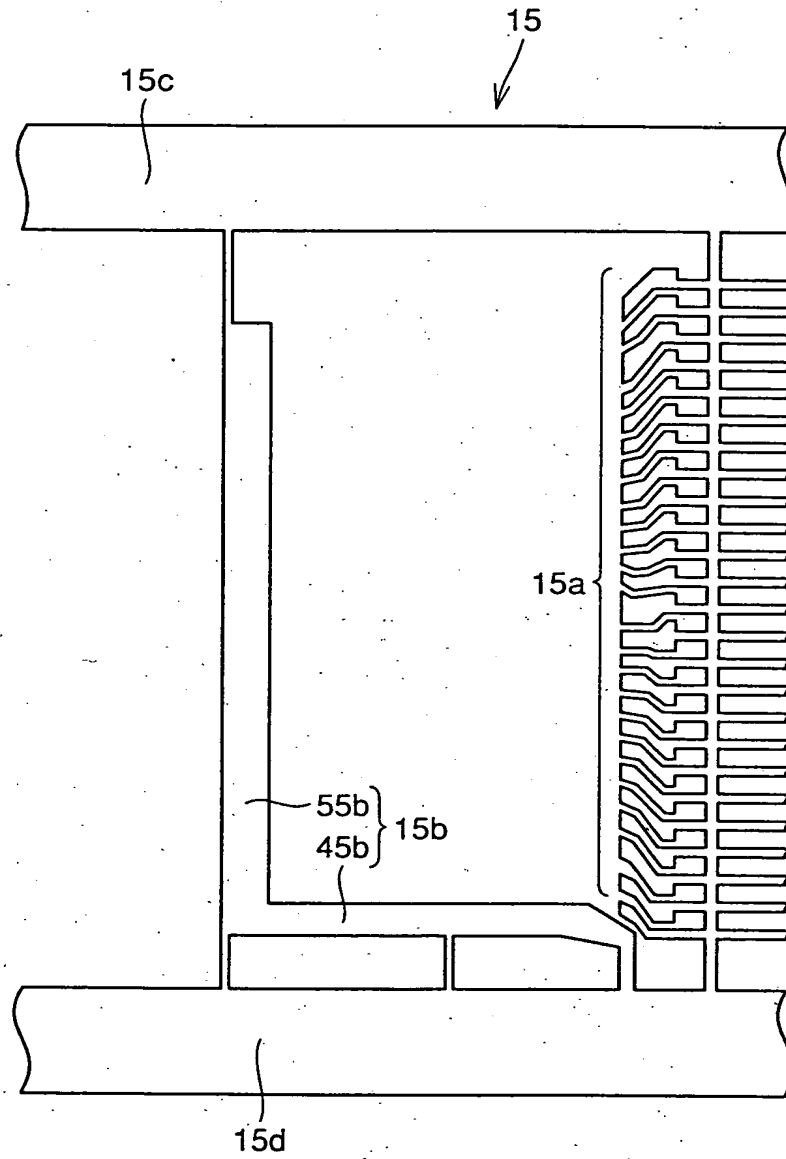


FIG. 26

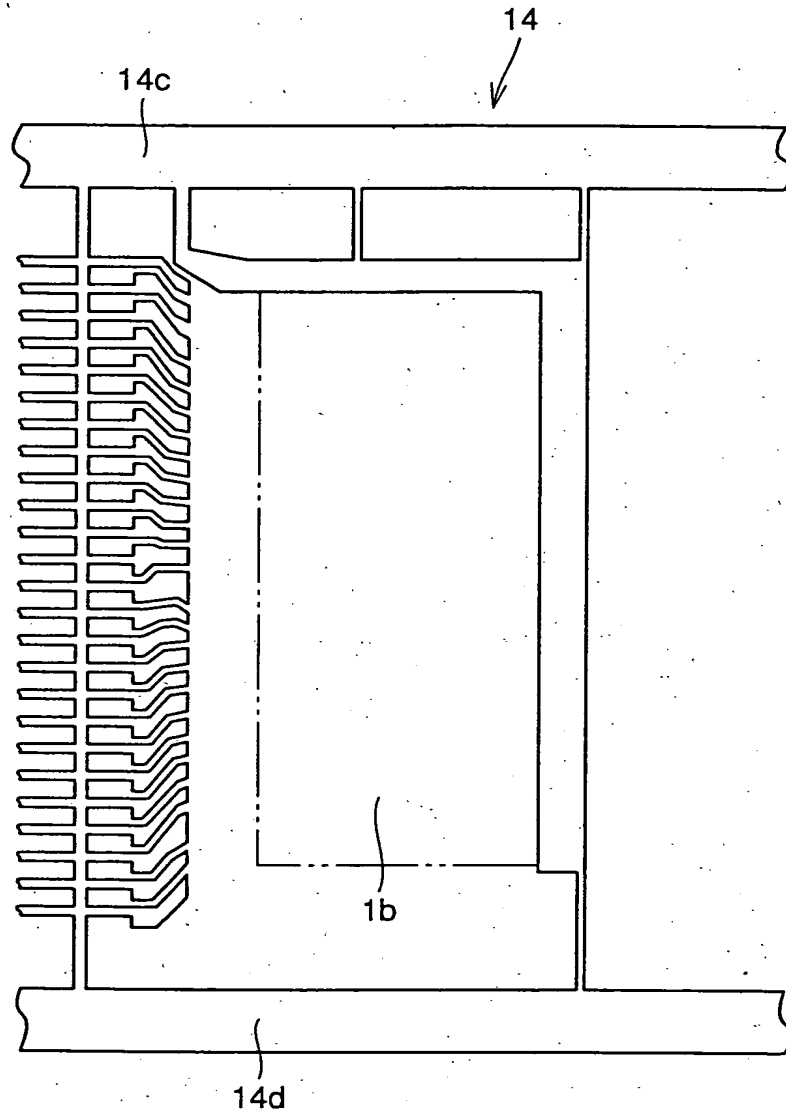


FIG. 27

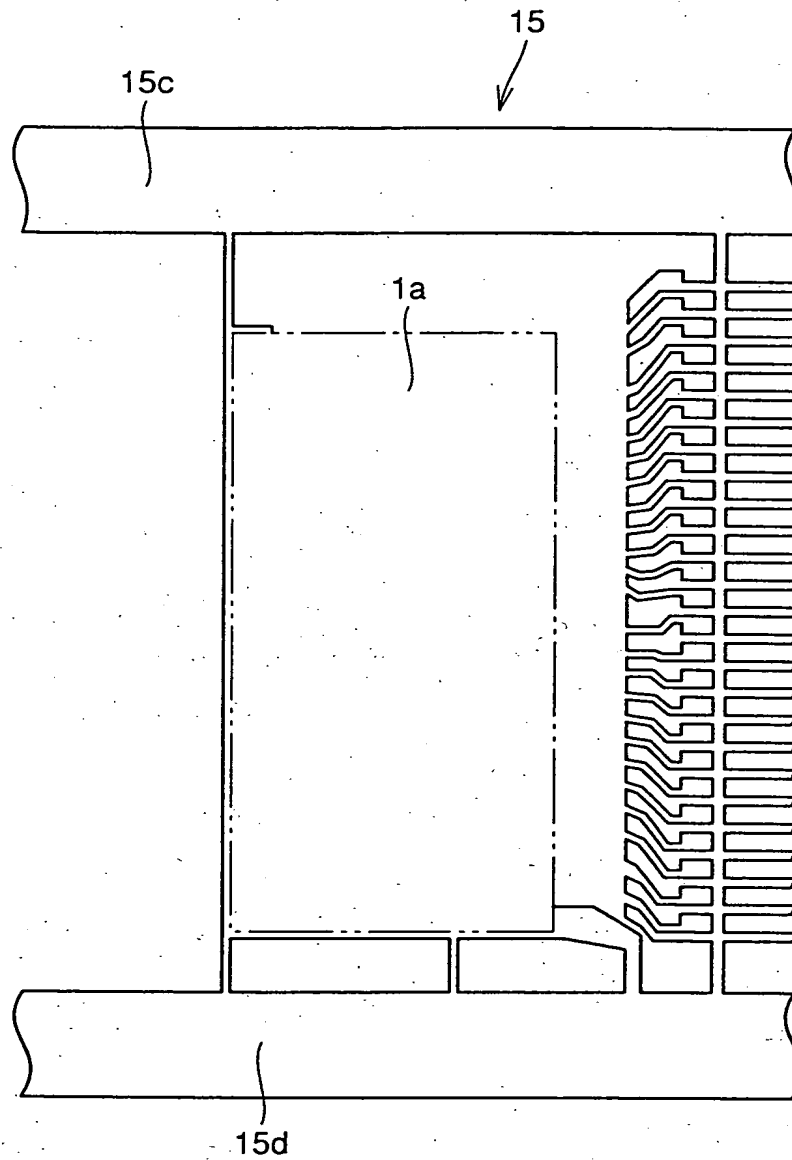


FIG. 28

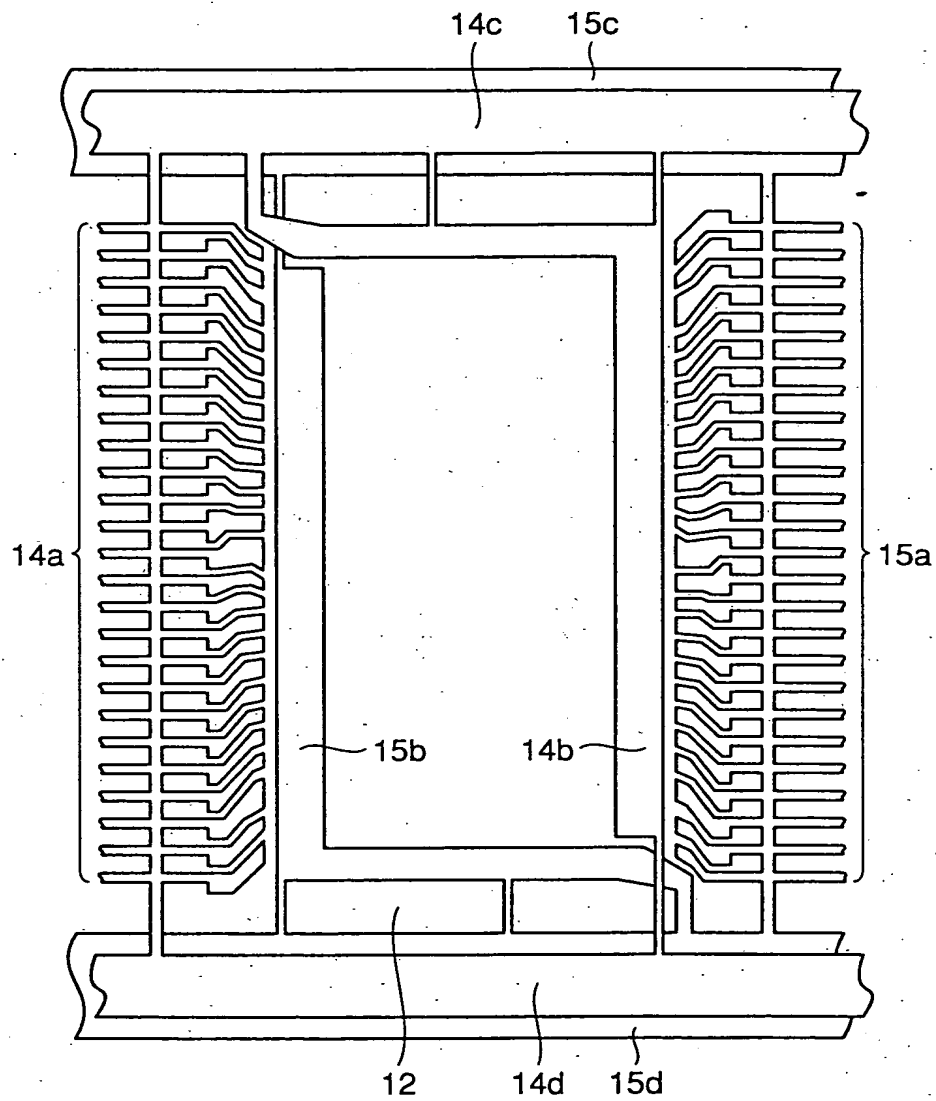


FIG. 29

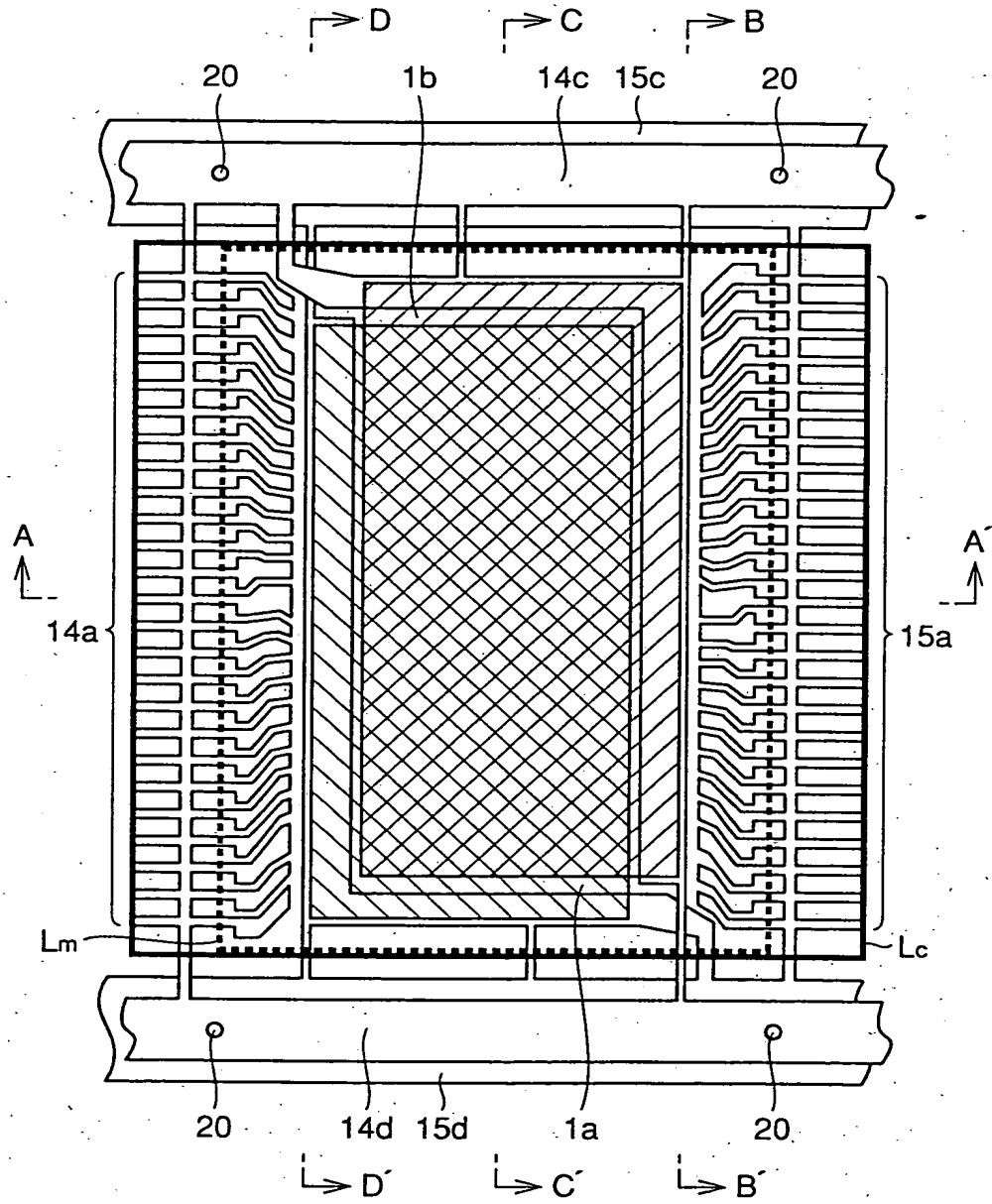


FIG. 30

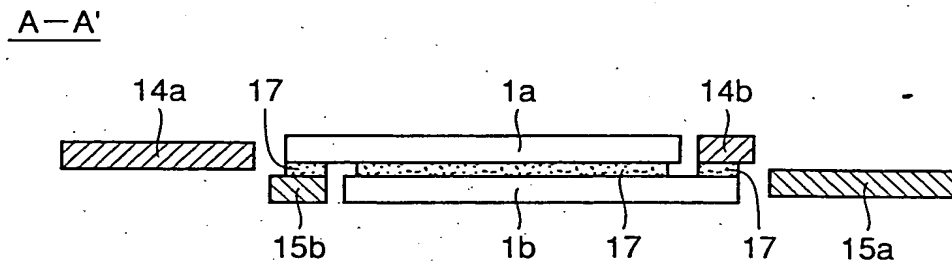


FIG. 31

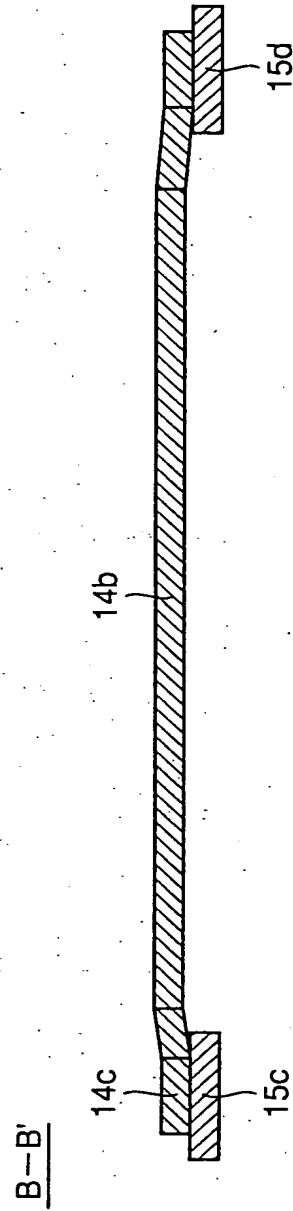


FIG. 32

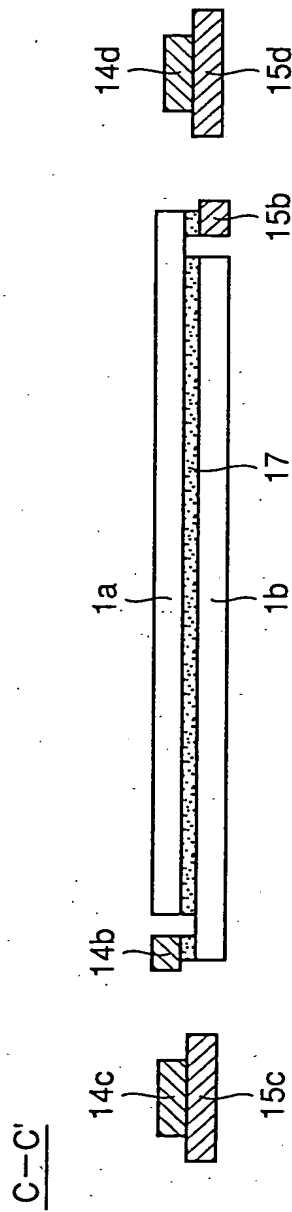


FIG. 33

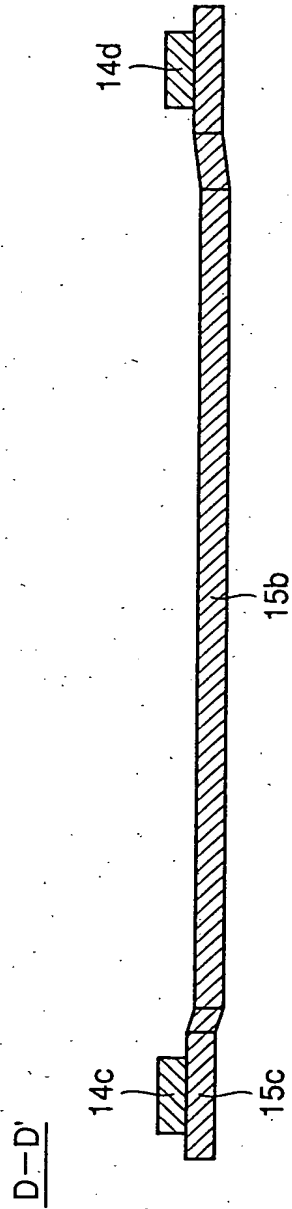


FIG. 34

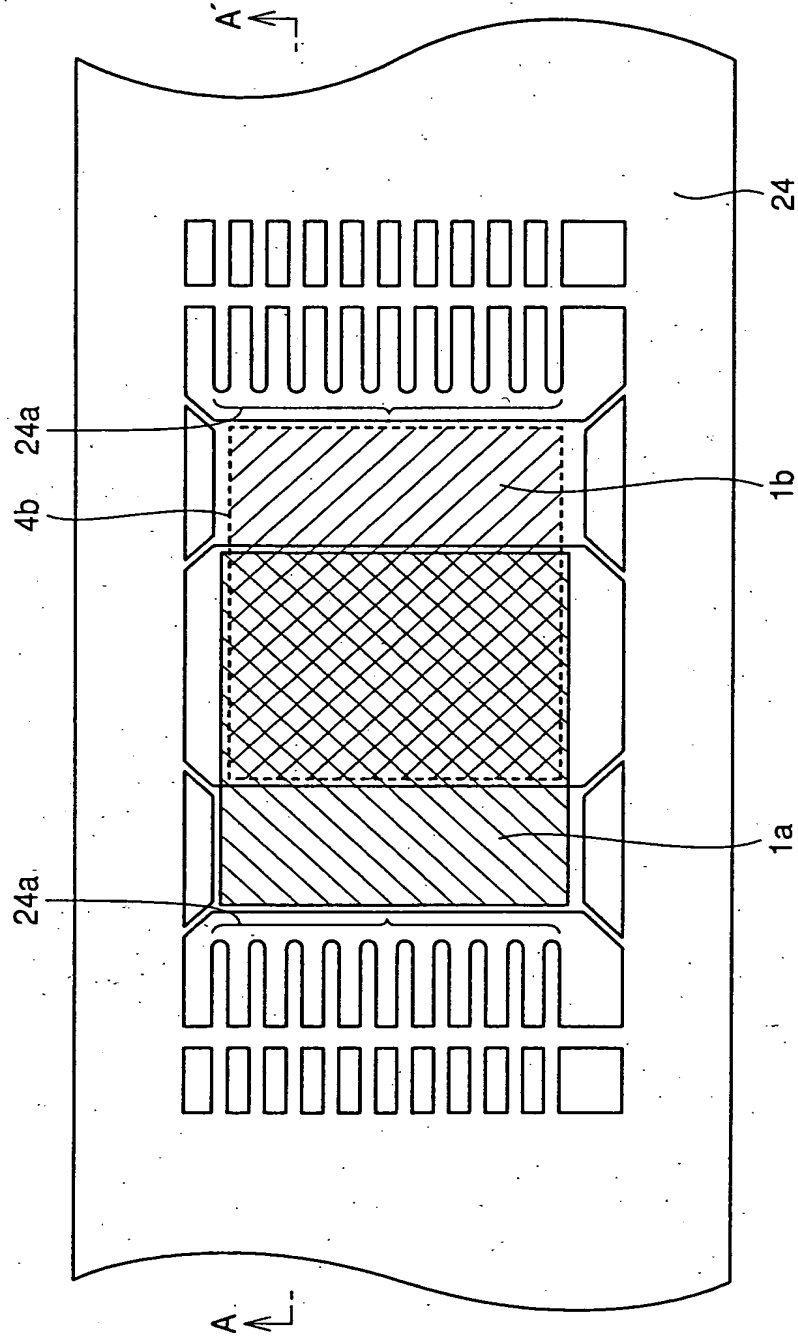


FIG. 35

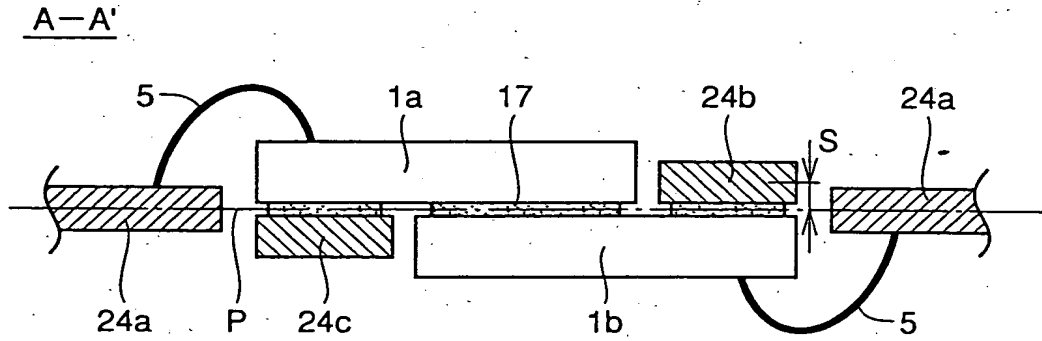


FIG. 36 PRIOR ART.

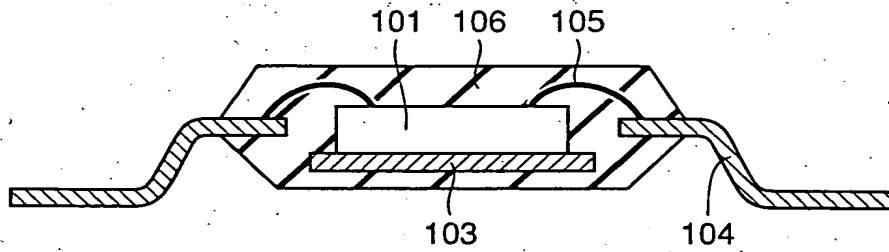


FIG. 37 PRIOR ART

